

06-29-01

Case Docket No. US 010320 A

THE COMMISSIONER FOR PATENTS, Washington, D.C. 20231

Enclosed for filing is the patent application of Inventor:  
JOHN PETRUZZELLO, THEODORE LETAVIC and MARK SIMPSON**FOR:** HV-SOI LDMOS DEVICE WITH INTEGRATED DIODE TO IMPROVE  
RELIABILITY AND AVALANCHE RUGGEDNESSjc821 U.S. PRO  
09/894083  
06/28/01**ENCLOSED ARE:**

- Appointment of Associates;
- Information Disclosure Statement, Form PTO-1449 and copies of documents listed therein;
- Preliminary Amendment;
- Specification (13 Pages of Specification, Claims, & Abstract);
- Declaration and Power of Attorney:  
(2 Pages of a  fully executed  unsigned Declaration);
- Drawing (4 sheets of  informal  formal sheet);
- Certified copy of Application No. ;
- Authorization Pursuant to 37 CFR §1.136(a)(3)
- Other: ;
- Assignment to KONINKLIJKE PHILIPS ELECTRONICS N.V.

## FEE COMPUTATION

## CLAIMS AS FILED

FOR	NUMBER FILED	NUMBER EXTRA	RATE	BASIC FEE - \$710.00
Total Claims	18 - 20 =		X \$18 =	
Independent Claims	4 - 3 =	1	X \$80 =	80.00
Multiple Dependent Claims, if any			\$270 =	
TOTAL FILING FEE . . . . .			=	\$ 790.00

Please charge Deposit Account No. 14-1270 in the amount of the total filing fee indicated above, plus any deficiencies. The Commissioner is also hereby authorized to charge any other fees which may be required, except the issue fee, or credit any overpayment to Account No. 14-1270.

Amend the specification by inserting before the first line as a centered heading --Cross Reference to Related Applications--; and insert below that as a new paragraph --This is a continuation-in-part of application Serial No. , filed , which is herein incorporated by reference--.



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